The XCalibur4740 is a secure, high-performance, 6U VPX-REDI, single board computer based on the Intel® Xeon® D-1700 series (formerly Ice Lake-D) of processors. The XCalibur4740 is an optimal choice for computationally heavy applications requiring maximum data protection.

The XCalibur4740 integrates SecureCOTS™ technology with a Microsemi® PolarFire™ FPGA for hosting custom functions to protect data from being modified or observed and provides an ideal solution when stringent security capabilities are required.

The XCalibur4740 provides incredible speed with two 40GBASE-KR4, two 1000BASE-X, and two 10/100/1000BASE-T Ethernet ports. It accommodates up to 48 GB of DDR4 ECC SDRAM in three channels and up to 32 GB of SLC NAND flash in addition to numerous I/O ports, including USB 2.0, PCIe, and RS-232/422/485 serial through the backplane connectors. The XCalibur4740 provides additional expansion capabilities by including two integrated XMC/PMC sites. These sites each include an x8 PCIe connection to the Intel® Xeon® D processor and X12d-X8d I/O mapped directly to the VPX backplane connectors. Additionally, each mezzanine site offers a single PMC connector, which provides a build option for P64s or X38s to the VPX backplane connectors.

Wind River VxWorks and X-ES Enterprise Linux (XEL) Board Support Packages (BSPs) are available.
Processor
- Intel® Xeon® D-1700 series (formerly Ice Lake-D) processor
- Up to 10 Xeon®-class cores in a single, power-efficient SoC package
- SKUs available with native extended temperature support

Memory
- 48 GB of DDR4 ECC SDRAM in three channels
- 32 GB of SLC NAND flash
- 64 MB NOR boot flash
- 64 kB EEPROM

Security and Management
- Microsemi® PolarFire™ FPGA with 128 MB SPI flash
- Designed with SecureCOTS™ technology to support enhanced security and trusted computing
- System voltage monitor, power-on/reset control, non-volatile write-protection control
- Trusted Platform Module (TPM)

VPX (VITA 46) P0 I/O
- Two IPMB connections to an IPMI Controller (IPMC)

VPX (VITA 46) P1 I/O
- Two general-purpose interrupts

VPX (VITA 46) P2 I/O
- Two x4 PCI Express Gen2-capable interfaces

VPX (VITA 46) P3 I/O
- Build option for PMC P24 I/O per VITA 46.9 P64s
- XMC P26 I/O per VITA 46.9 X38s

VPX (VITA 46) P4 I/O
- Two RS-232/422/485 serial ports
- Build option for PMC P14 I/O per VITA 46.9 P64s, or XMC P16 I/O per VITA 46.9 X38s

VPX (VITA 46) P5 I/O
- Two USB 2.0 ports
- One x4 PCI Express Gen2-capable interface

VPX (VITA 46) P6 I/O
- XMC P16 I/O per VITA 46 x12d+x8d
- Two USB 2.0 ports
- One x4 PCI Express Gen2-capable interface

VPX (VITA 46) P7 I/O
- Two RS-232/422/485 serial ports
- Build option for PMC P14 I/O per VITA 46.9 P64s, or XMC P16 I/O per VITA 46.9 X38s

XMCalibur4740
- x8 PCI Express Gen3-capable port to J15 and J25
- Pn6 I/O to VPX connectors per VITA 46.9 X8d+X12d
- Build option for Pn6 I/O to VPX connectors, per VITA 46.9 X38s

PrPMC
- Build option for Prn4 I/O to VPX connectors per VITA 46.9 P64s

Software Support
- UEFI firmware
- Wind River VxWorks BSP
- X-ES Enterprise Linux (XEL) BSP
- Contact factory for availability of Green Hills INTEGRITY, QNX Neutrino, and LynuxWorks LynxOS BSPs, as well as Microsoft Windows drivers

Environmental Requirements
- Contact factory for appropriate board configuration based on environmental requirements
- Supported ruggedization levels (see chart below): 5
- Conformal coating available as an ordering option
- Thermal performance will vary based on CPU frequency and application
- Contact X-ES for air-cooled development options

Physical Characteristics
- Contact factory for details

Power Requirements
- Power will vary based on configuration and usage. Please consult factory.

Ruggedization Level | Level 5
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Cooling Method | Conduction-Cooled
Operating Temperature | -40 to +85°C (board rail surface)
Storage Temperature | -55 to +105°C (maximum)
Vibration | 0.1 g²/Hz (maximum), 5 to 2000 Hz
Shock | 40 g, 11 ms sawtooth
Humidity | Up to 95% non-condensing

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