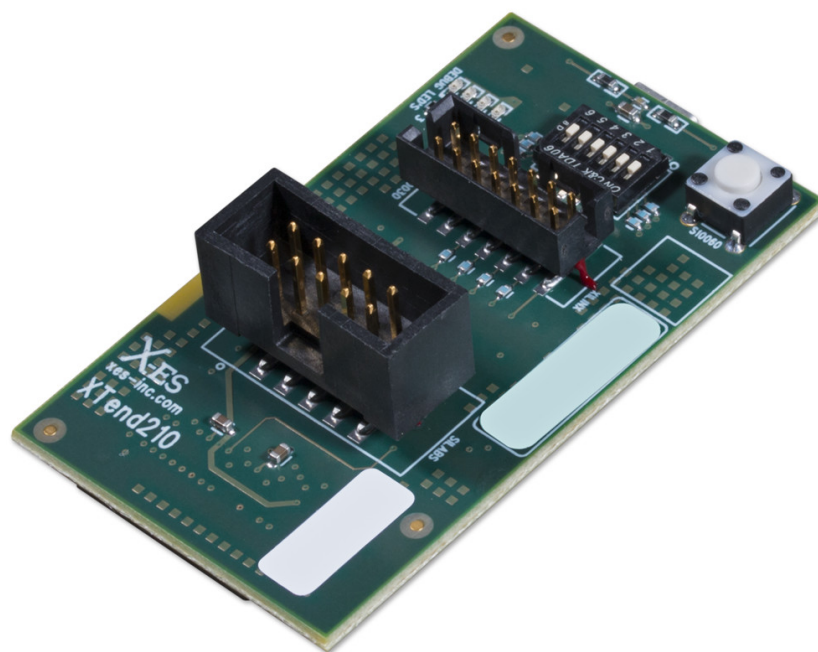


XTend210

Debug Module for Xilinx FPGA Cards

- ▶ Integrated Digilent programmer available via USB port
- ▶ JTAG header for Xilinx programmers
- ▶ Serial over USB for host cards that support UART connection
- ▶ Push button for Xilinx Zynq® UltraScale+™ MPSoC reset
- ▶ Six general-purpose switches and four general-purpose debug LEDs
- ▶ Ribbon cable header to host card



XTend210

The XTend210 provides JTAG access for FPGA configuration and debug.

An integrated Digilent programmer is available via one USB port, while Xilinx programmers can be connected to a Xilinx FPGA programming header. For FPGA host cards that support a UART connection, the XTend210 provides serial access via the USB port.

The XTend210 also includes a push button that can be used in conjunction with the PS_SRST_B pin on the Xilinx Zynq® UltraScale+™ MPSoC to allow debugging software to reset the processor core of the Zynq® UltraScale+™ MPSoC.

A flexible ribbon cable connects the XTend210 to supported FPGA host cards.

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I/O Breakout

- Integrated Digilent programmer available via USB port
- JTAG header for Xilinx programmers
- Serial over USB for host cards that support UART connection to debug module
- Ribbon cable header to host card

Additional Features

- Push button for Xilinx Zynq® UltraScale+™ MPSoC reset
- Six general-purpose switches and four general-purpose debug LEDs

