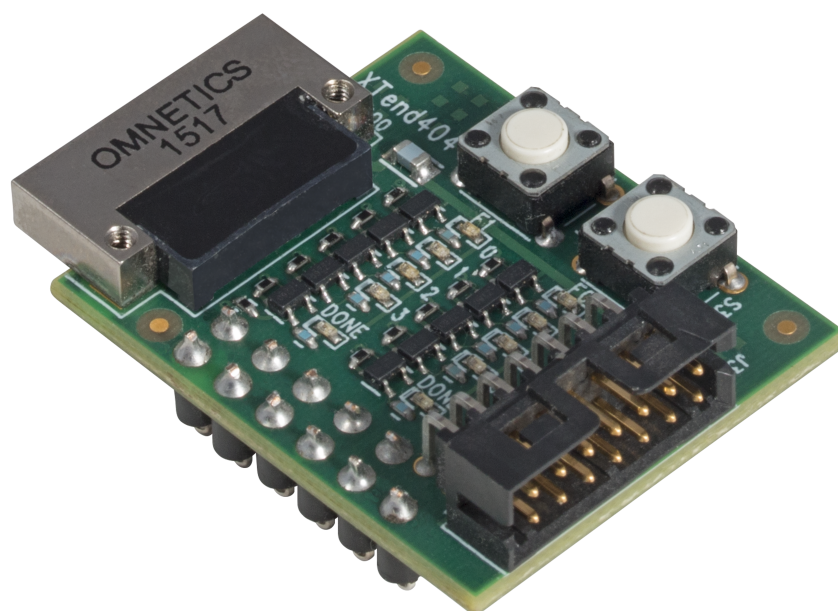


XTend404

Debug Module for AMD (formerly Xilinx) FPGA-Based Cards

- › JTAG header for AMD (formerly Xilinx) programmers
- › Omnetics connector to host card
- › LED connections to FPGA DONE signals
- › Additional LEDs and test points connected to FPGA I/O
- › Push buttons to force FPGA reconfiguration



XTend404

The XTend404 provides JTAG access for FPGA configuration and debug for select X-ES modules with up to two AMD (formerly Xilinx) FPGAs. An AMD FPGA programming header supports AMD platform USB and Digilent JTAG-HS3 programmers. LEDs connected to each FPGA's DONE signal indicate successful completion of FPGA configuration.

Additional LEDs and test points connected to FPGA I/O can be used for general-purpose debug, while push buttons can be used to force FPGA reconfiguration.

X-ES

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I/O Breakout

- JTAG header for AMD (formerly Xilinx) programmers
- Omnetics connector to host card
- LED connection to each FPGA's DONE signal
- Additional LEDs and test points connected to FPGA I/O
- Push buttons to force FPGA reconfiguration

